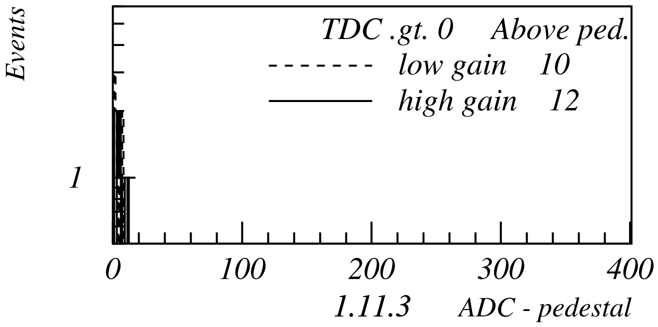
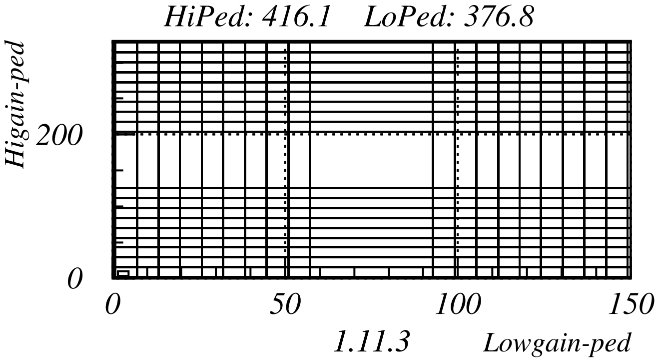
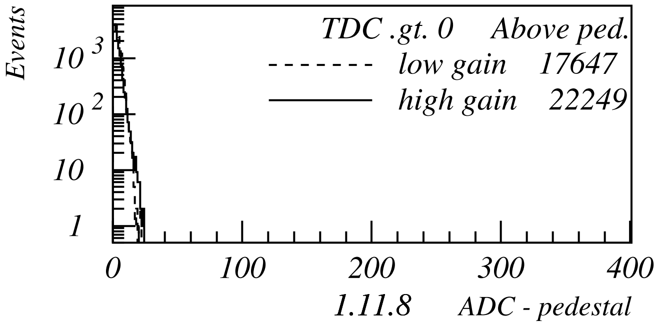
Prod 8553.1



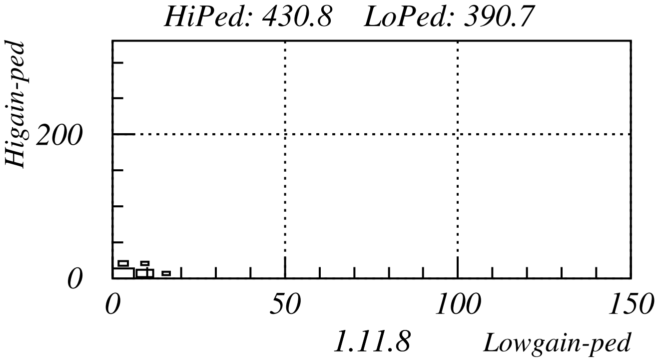
Events
TDC .gt. 0 Above ped.
low gain 10
high gain 12
1
0 100 200 300 400
1.11.3 ADC - pedestal



HiPed: 416.1 LoPed: 376.8
Higain-ped
200
0
0 50 100 150
1.11.3 Lowgain-ped

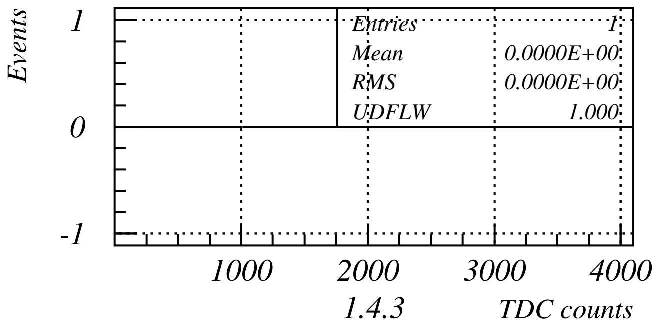


Events
TDC .gt. 0 Above ped.
low gain 17647
high gain 22249
10 3
10 2
10
1
0 100 200 300 400
1.11.8 ADC - pedestal

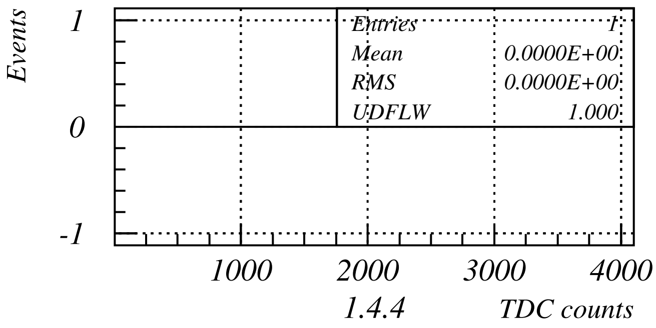


HiPed: 430.8 LoPed: 390.7
Higain-ped
200
0
0 50 100 150
1.11.8 Lowgain-ped

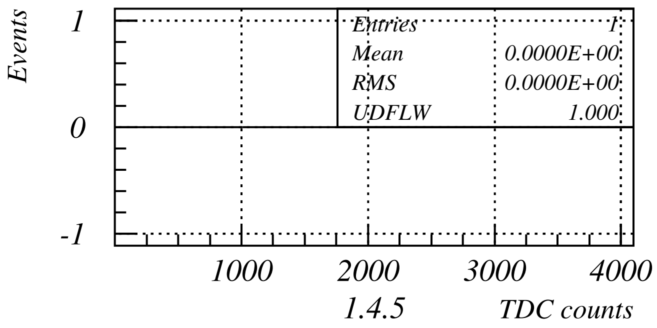
Prod 8553.1



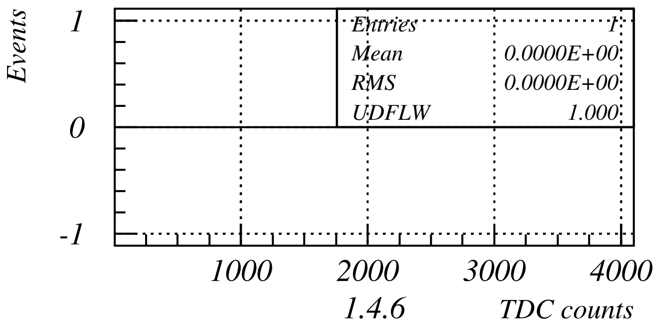
1.4.3



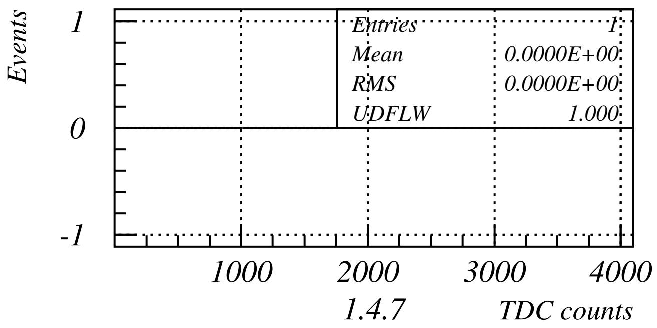
1.4.4



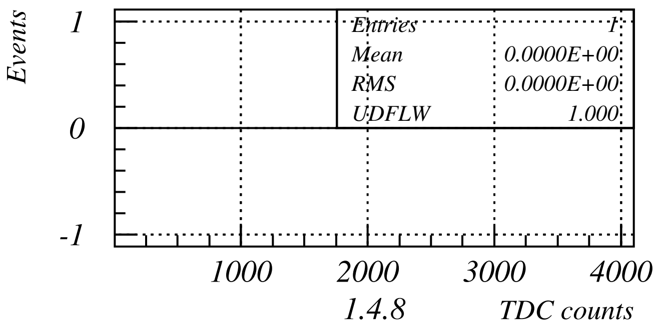
1.4.5



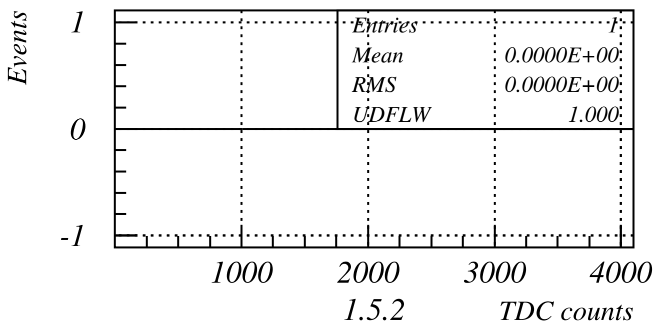
1.4.6



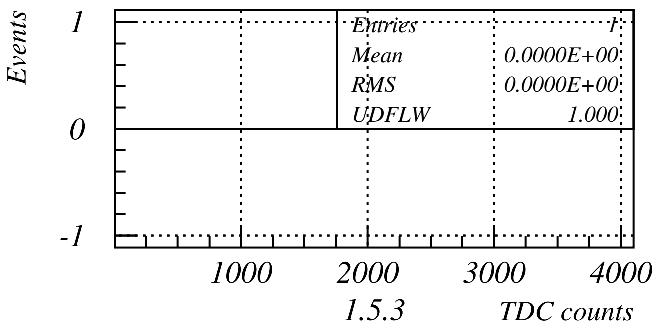
1.4.7



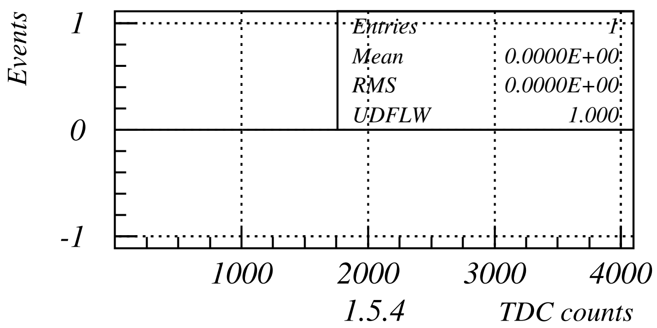
1.4.8



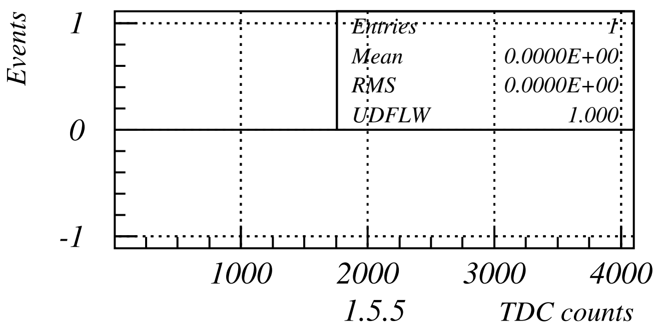
1.5.2



1.5.3

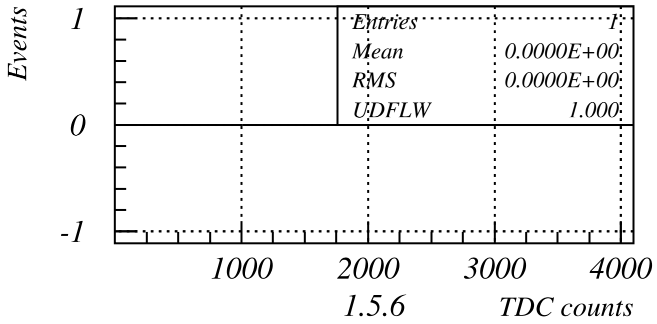


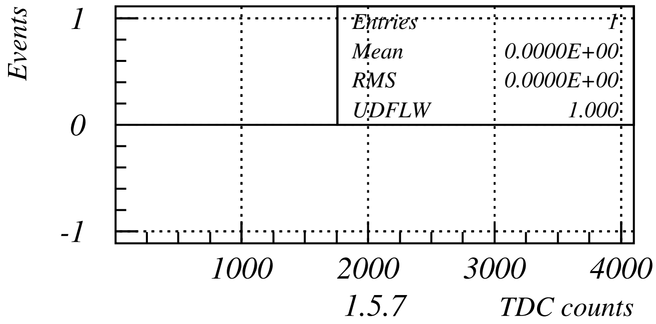
1.5.4



1.5.5

Prod 8553.1


Events
1
0
-1
Entries 1
Mean 0.0000E+00
RMS 0.0000E+00
UDFLW 1.000
1000 2000 3000 4000
1.5.6
TDC counts


Events
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0
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Entries 1
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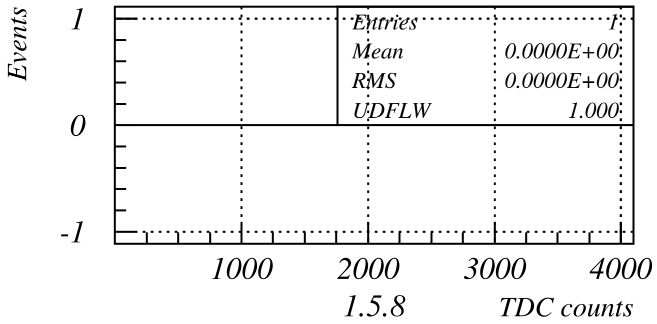
RMS 0.0000E+00
UDFLW 1.000
1000 2000 3000 4000
1.5.7
TDC counts


Events
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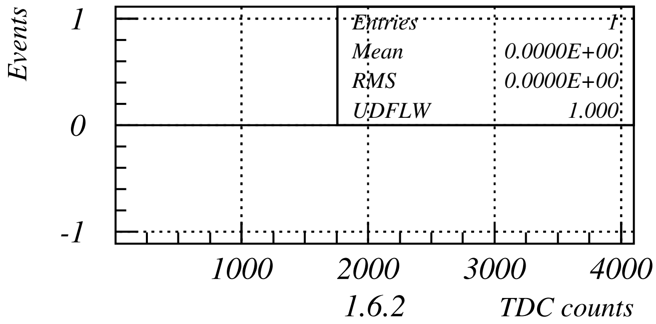
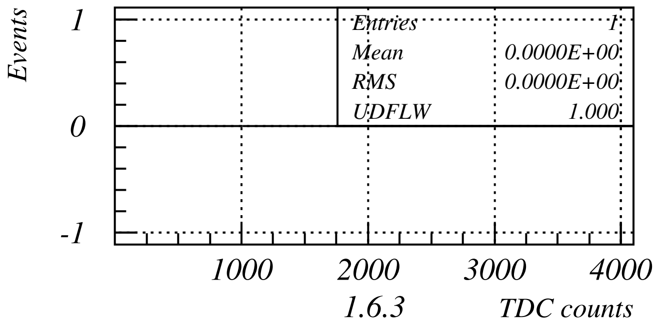
0
-1
Entries 1
Mean 0.0000E+00
RMS 0.0000E+00
UDFLW 1.000
1000 2000 3000 4000
1.5.8
TDC counts


Events
1
0
-1
Entries 1
Mean 0.0000E+00
RMS 0.0000E+00
UDFLW 1.000
1000 2000 3000 4000
1.6.2
TDC counts


Events
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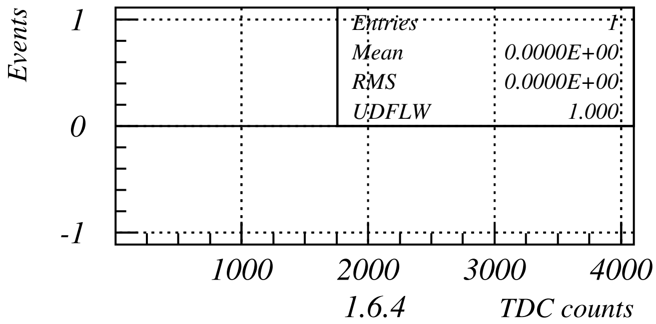
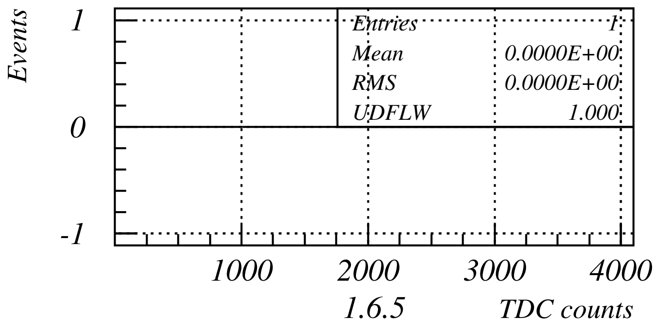
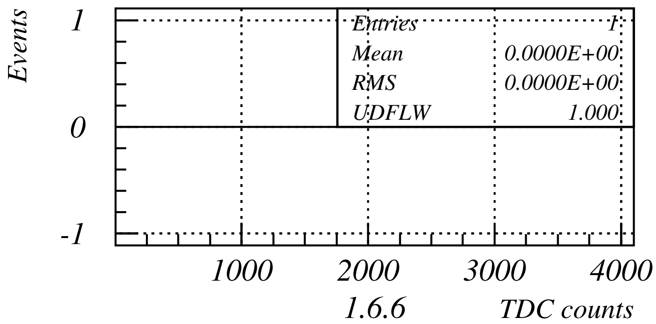
0
-1
Entries 1
Mean 0.0000E+00
RMS 0.0000E+00
UDFLW 1.000
1000 2000 3000 4000
1.6.3
TDC counts


Events
1
0
-1
Entries 1
Mean 0.0000E+00
RMS 0.0000E+00
UDFLW 1.000
1000 2000 3000 4000
1.6.4
TDC counts


Events
1
0
-1
Entries 1
Mean 0.0000E+00
RMS 0.0000E+00
UDFLW 1.000
1000 2000 3000 4000
1.6.5
TDC counts


Events
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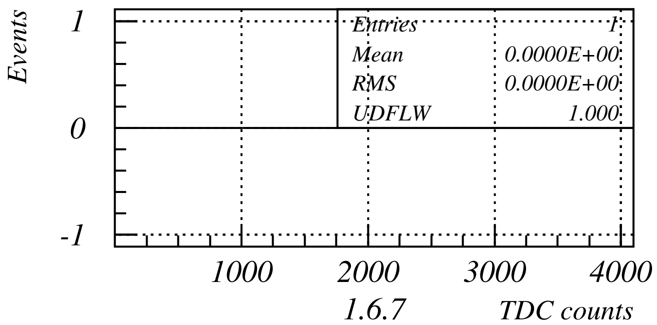
-1
Entries 1
Mean 0.0000E+00
RMS 0.0000E+00
UDFLW 1.000
1000 2000 3000 4000
1.6.6
TDC counts


Events
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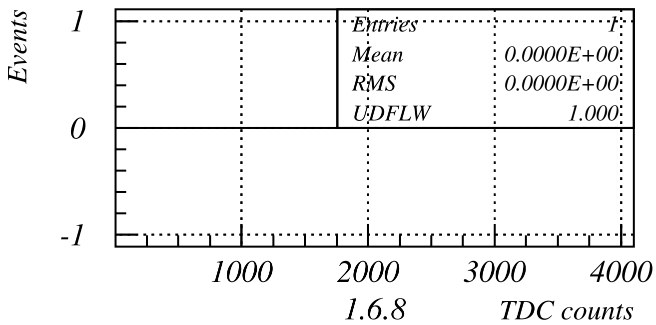
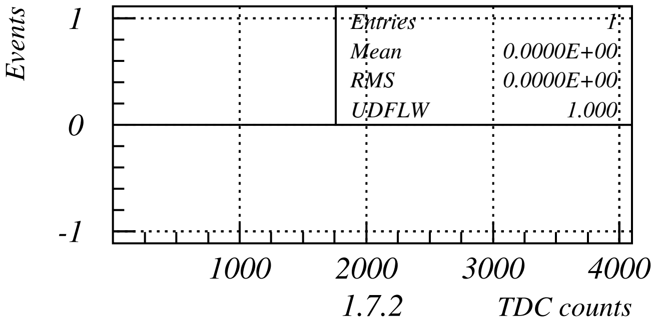
0
-1
Entries 1
Mean 0.0000E+00
RMS 0.0000E+00
UDFLW 1.000
1000 2000 3000 4000
1.6.7
TDC counts


Events
1
0
-1
Entries 1
Mean 0.0000E+00
RMS 0.0000E+00
UDFLW 1.000
1000 2000 3000 4000
1.6.8
TDC counts

Prod 8553.1


Events
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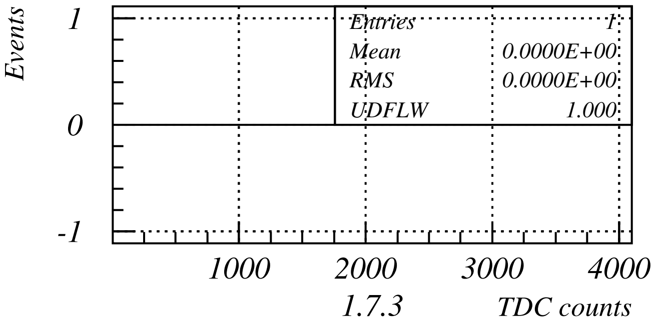
Entries 1
Mean 0.0000E+00
RMS 0.0000E+00
UDFLW 1.000
1000 2000 3000 4000
1.7.2
TDC counts


Events
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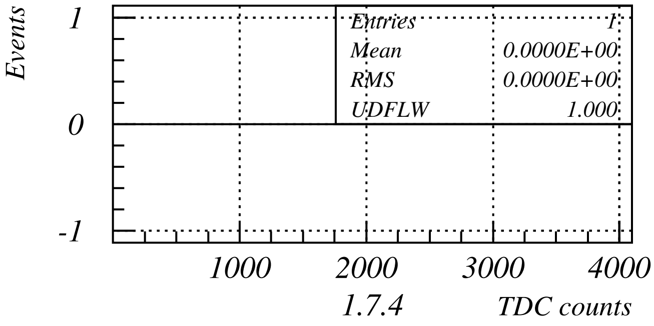
0
-1
Entries 1
Mean 0.0000E+00
RMS 0.0000E+00
UDFLW 1.000
1000 2000 3000 4000
1.7.3
TDC counts


Events
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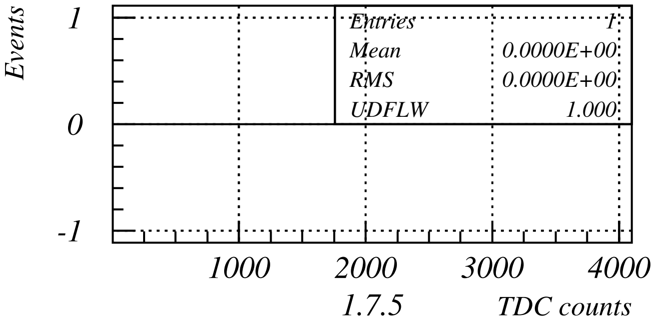
0
-1
Entries 1
Mean 0.0000E+00
RMS 0.0000E+00
UDFLW 1.000
1000 2000 3000 4000
1.7.4
TDC counts


Events
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0
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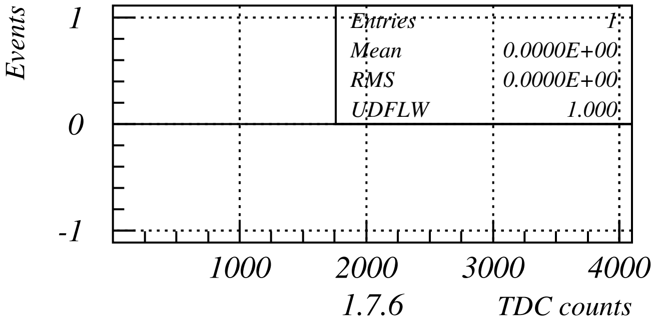
Entries 1
Mean 0.0000E+00
RMS 0.0000E+00
UDFLW 1.000
1000 2000 3000 4000
1.7.5
TDC counts


Events
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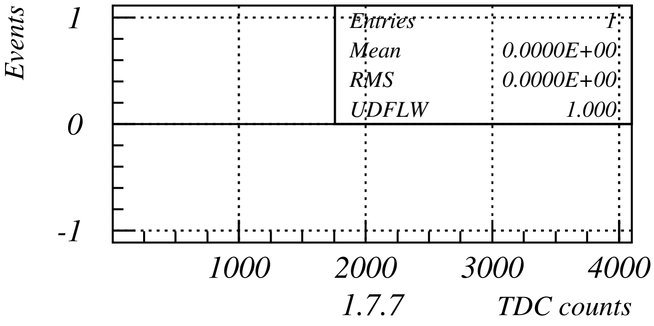
Entries 1
Mean 0.0000E+00
RMS 0.0000E+00
UDFLW 1.000
1000 2000 3000 4000
1.7.6
TDC counts


Events
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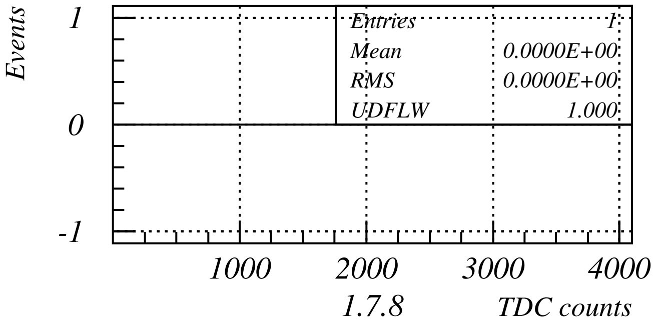
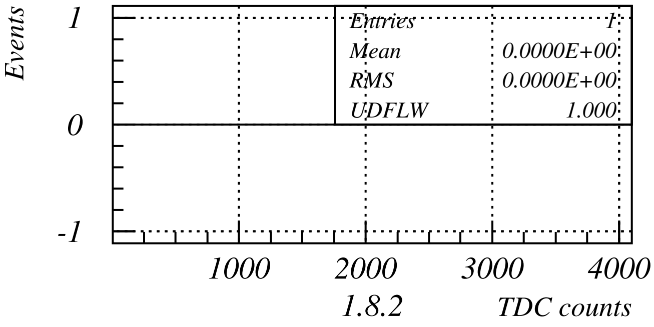
0
-1
Entries 1
Mean 0.0000E+00
RMS 0.0000E+00
UDFLW 1.000
1000 2000 3000 4000
1.7.7
TDC counts


Events
1
0
-1
Entries 1
Mean 0.0000E+00
RMS 0.0000E+00
UDFLW 1.000
1000 2000 3000 4000
1.7.8
TDC counts


Events
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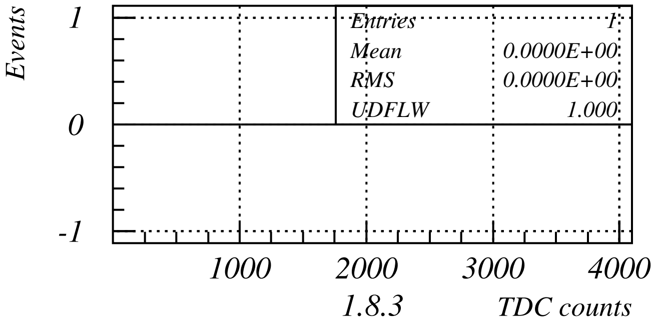
-1
Entries 1
Mean 0.0000E+00
RMS 0.0000E+00
UDFLW 1.000
1000 2000 3000 4000
1.8.2
TDC counts


Events
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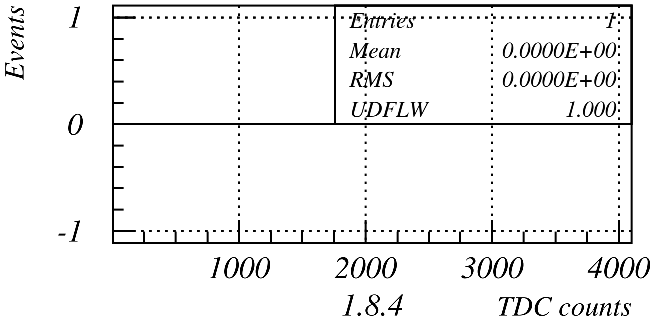
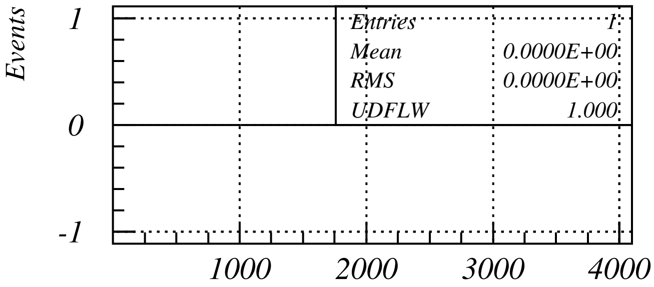
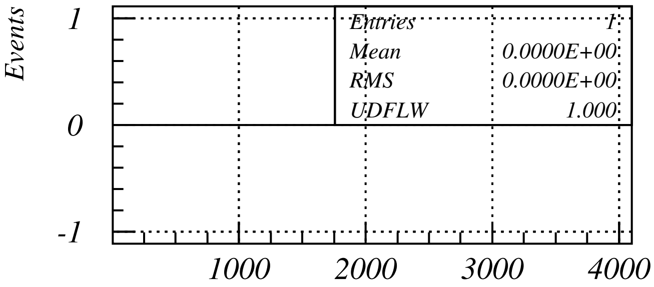
-1
Entries 1
Mean 0.0000E+00
RMS 0.0000E+00
UDFLW 1.000
1000 2000 3000 4000
1.8.3
TDC counts


Events
1
0
-1
Entries 1
Mean 0.0000E+00
RMS 0.0000E+00
UDFLW 1.000
1000 2000 3000 4000
1.8.4
TDC counts

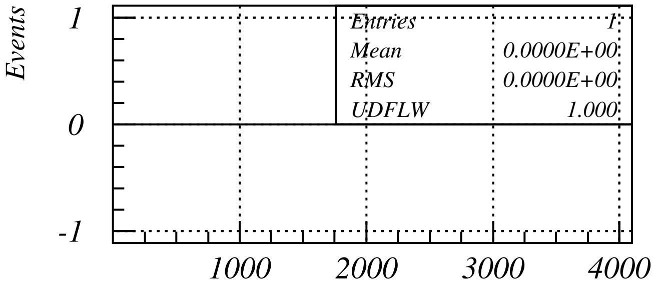
Prod 8553.1



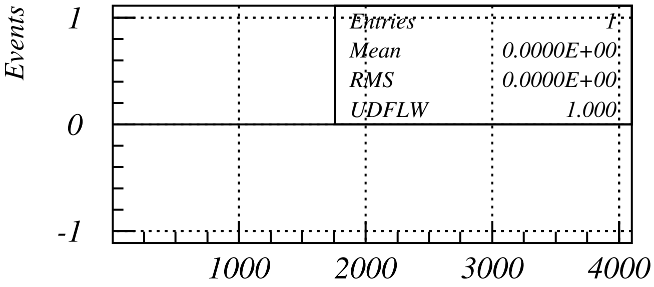
1.8.5



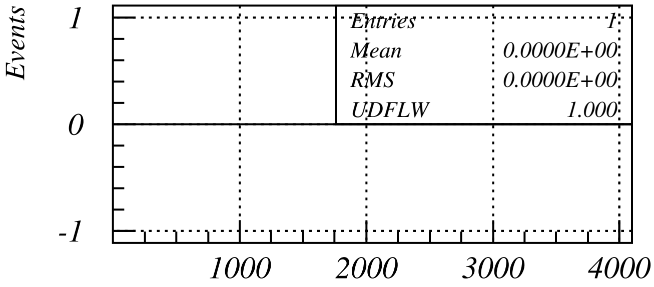
1.8.6



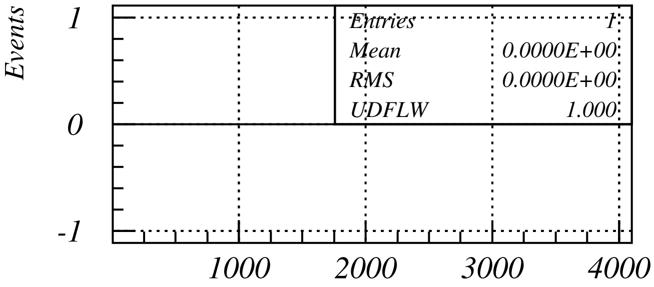
1.8.7



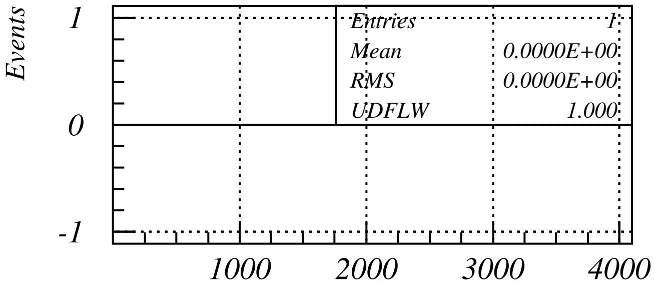
1.8.8



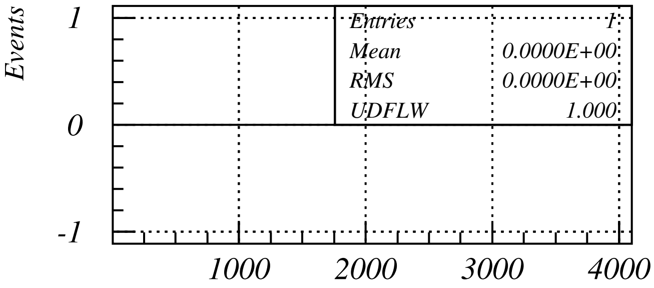
1.9.2



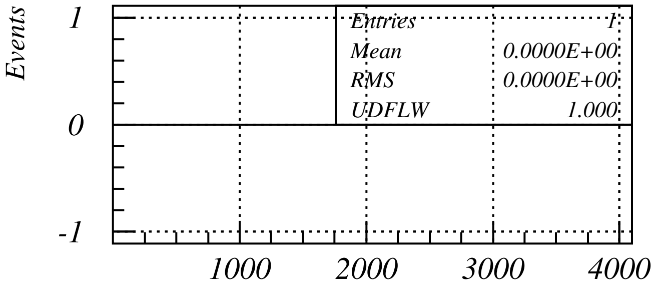
1.9.3



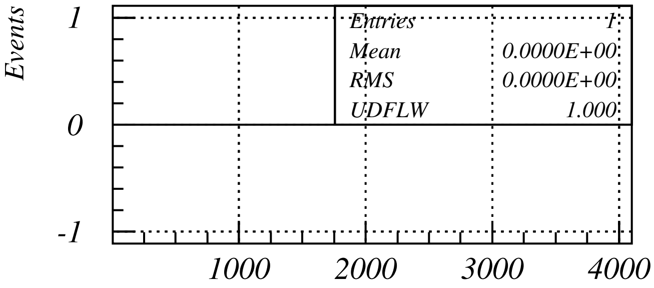
1.9.4



1.9.5

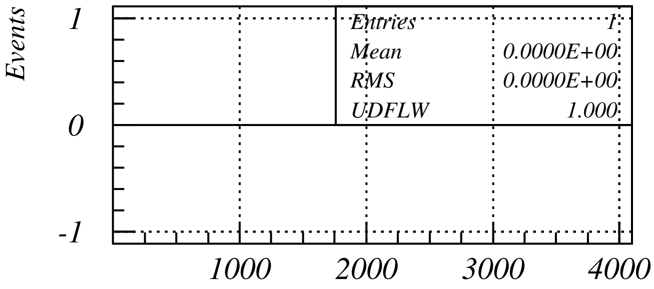


1.9.6

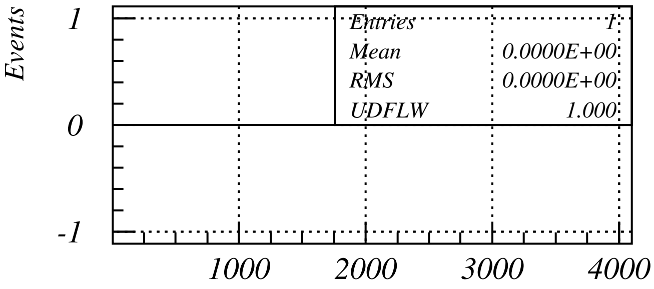


1.9.7

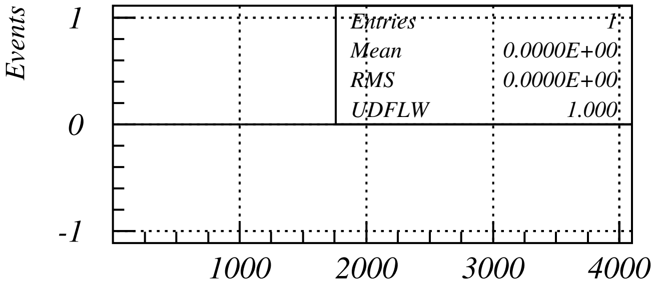
Prod 8553.1



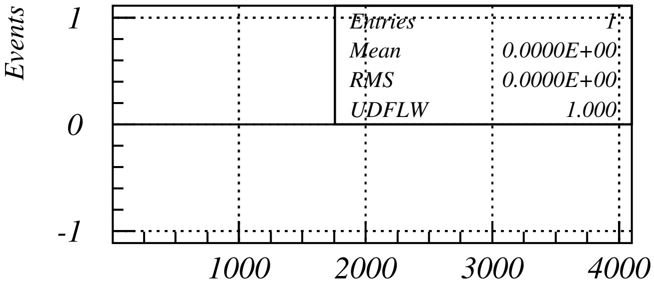
1.9.8 TDC counts



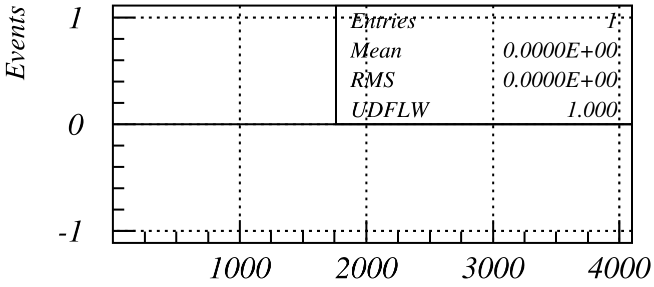
1.10.2 TDC counts



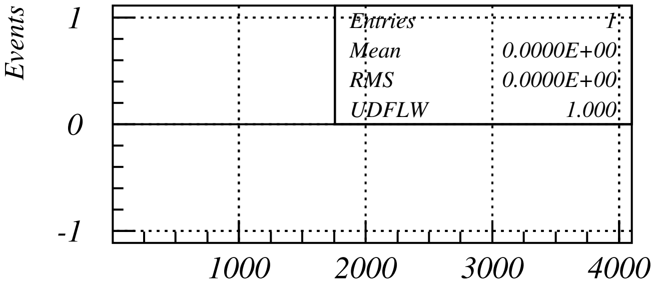
1.10.3 TDC counts



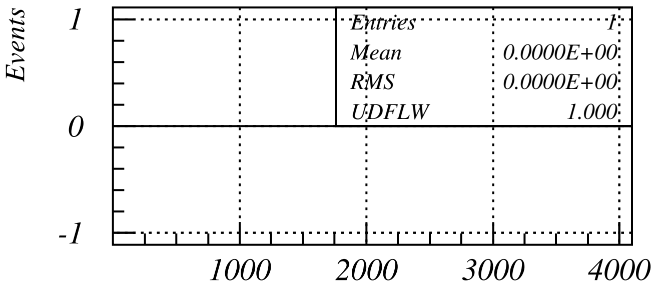
1.10.4 TDC counts



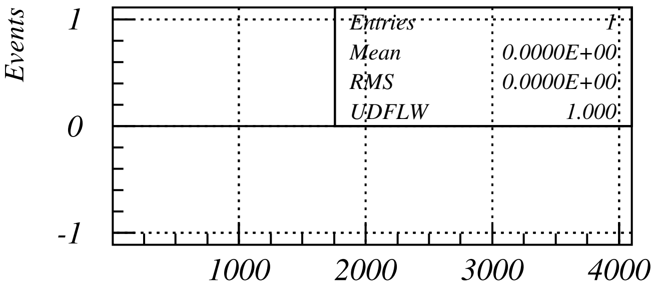
1.10.5 TDC counts



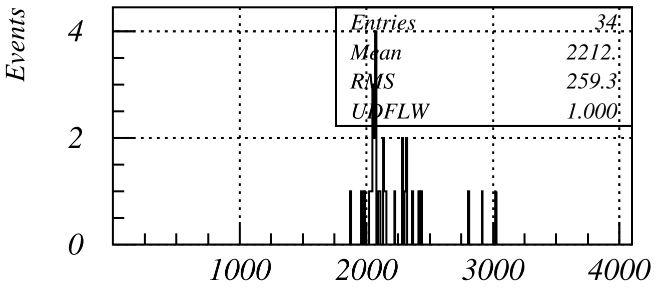
1.10.6 TDC counts



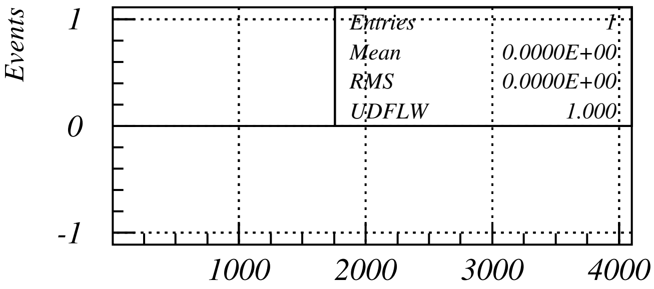
1.10.7 TDC counts



1.10.8 TDC counts

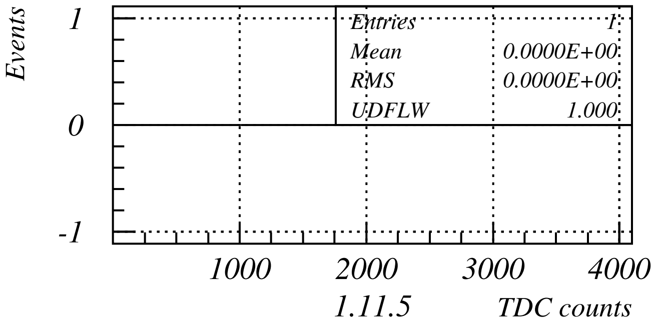


1.11.3 TDC counts

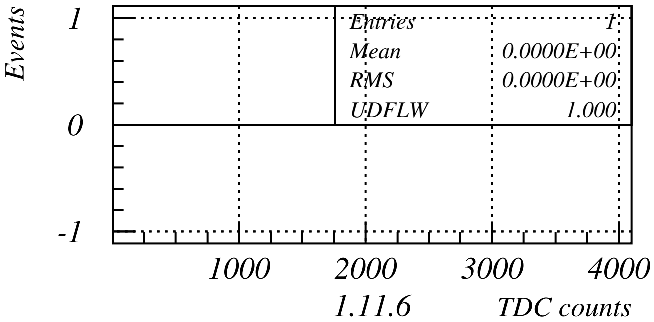


1.11.4 TDC counts

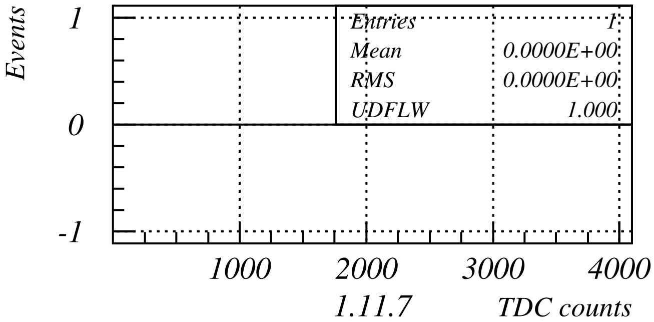
Prod 8553.1



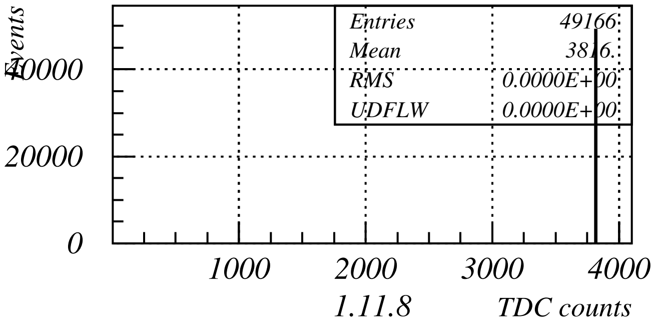
Events
Entries 1
Mean 0.0000E+00
RMS 0.0000E+00
UDFLW 1.000
1
0
-1
1000
2000
3000
4000
1.11.5
TDC counts



Events
Entries 1
Mean 0.0000E+00
RMS 0.0000E+00
UDFLW 1.000
1
0
-1
1000
2000
3000
4000
1.11.6
TDC counts



Events
Entries 1
Mean 0.0000E+00
RMS 0.0000E+00
UDFLW 1.000
1
0
-1
1000
2000
3000
4000
1.11.7
TDC counts



Events
Entries 49166
Mean 3816.
RMS 0.0000E+00
UDFLW 0.0000E+00
40000
20000
0
1000
2000
3000
4000
1.11.8
TDC counts